

FIG. 3(a)

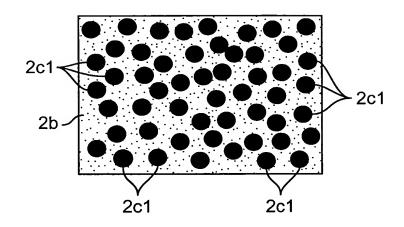
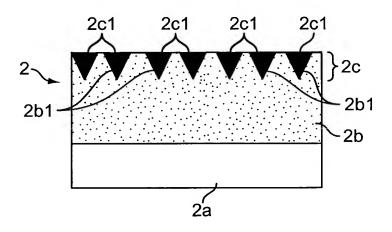


FIG. 3(b)



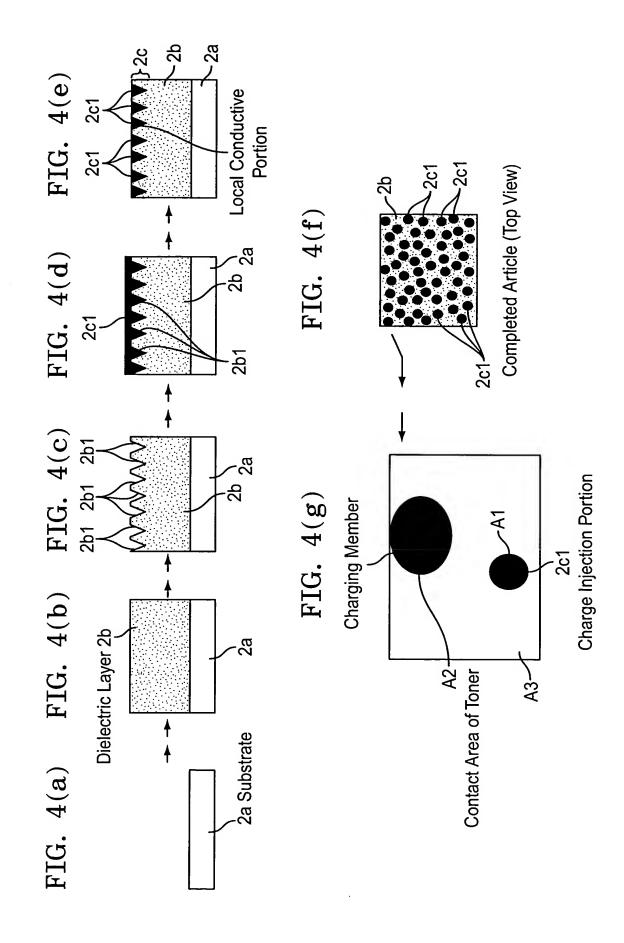


FIG. 5(a) FIG. 5(b) FIG. 5(c)

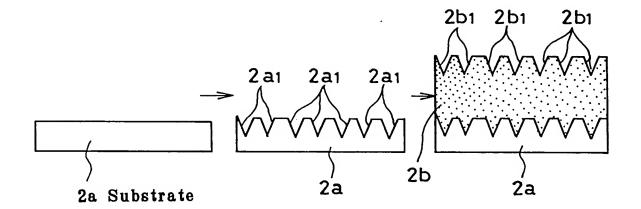


FIG. 6(a)

FIG. 6(b)

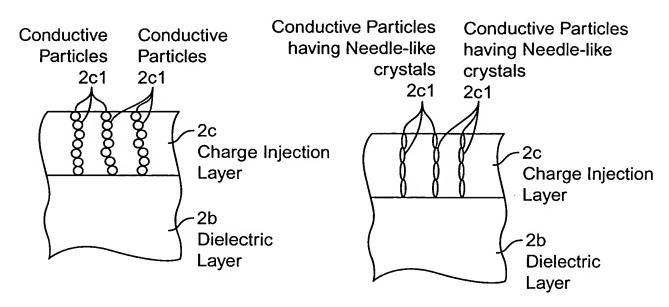


FIG. 7(a)

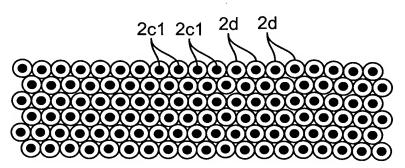


FIG. 7(b)

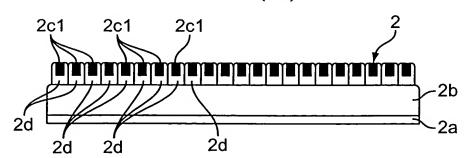


FIG. 8(a)

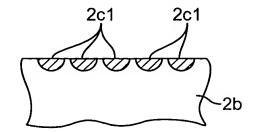


FIG. 8(b)

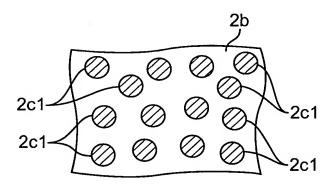


FIG. 9(a)

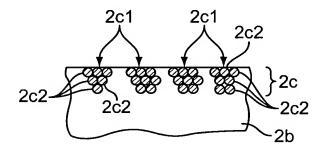


FIG. 9(b)

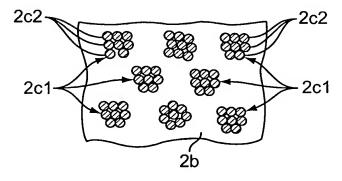
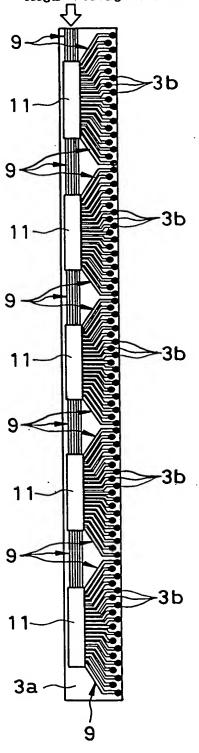
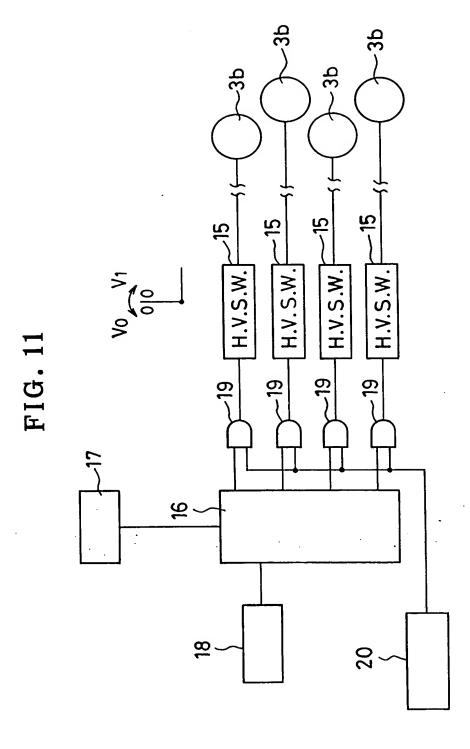


FIG. 10

Line Data Signal Writing Timing Signal High Voltage Power





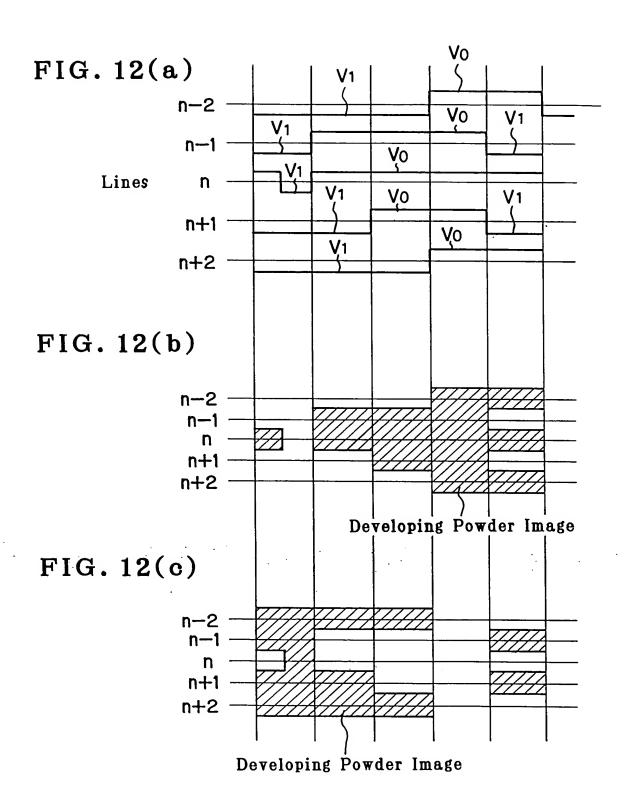


FIG. 13

Example (1), Thickness of Charge Injection Layer: $1\mu m$

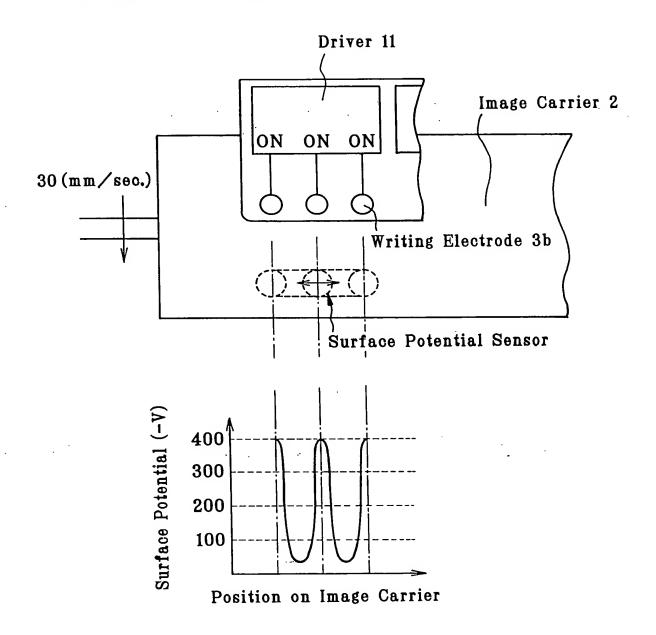


FIG. 14

Example (2), Thickness of Charge Injection Layer: $10\mu m$

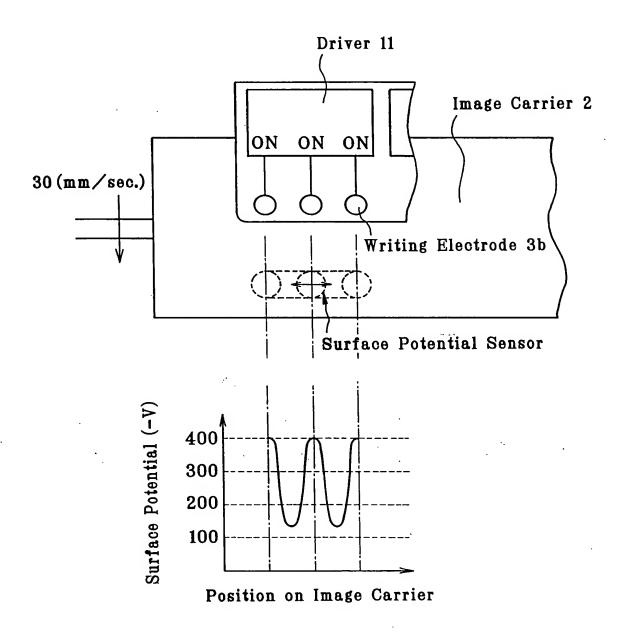


FIG. 15

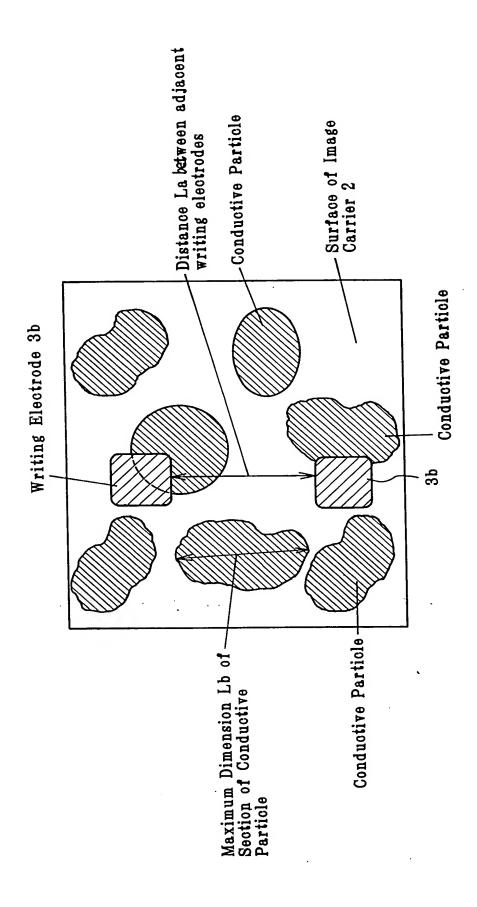


FIG. 16(a)

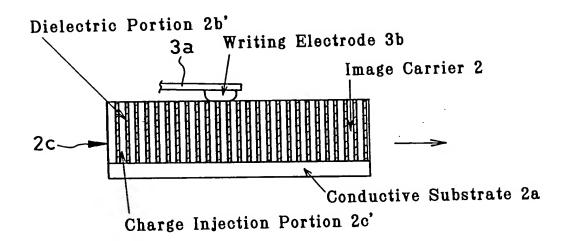


FIG. 16(b)

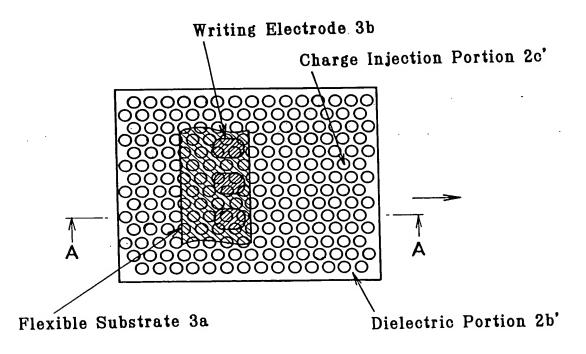


FIG. 17(a)

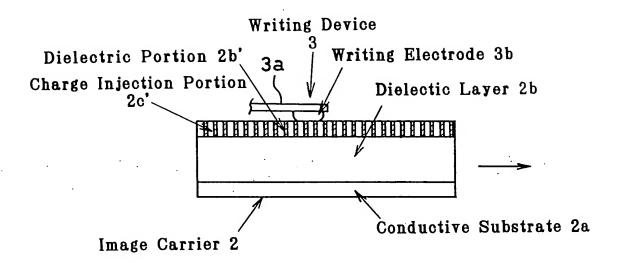
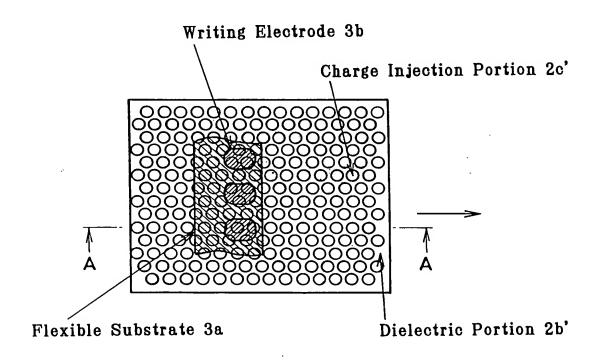


FIG. 17(b)



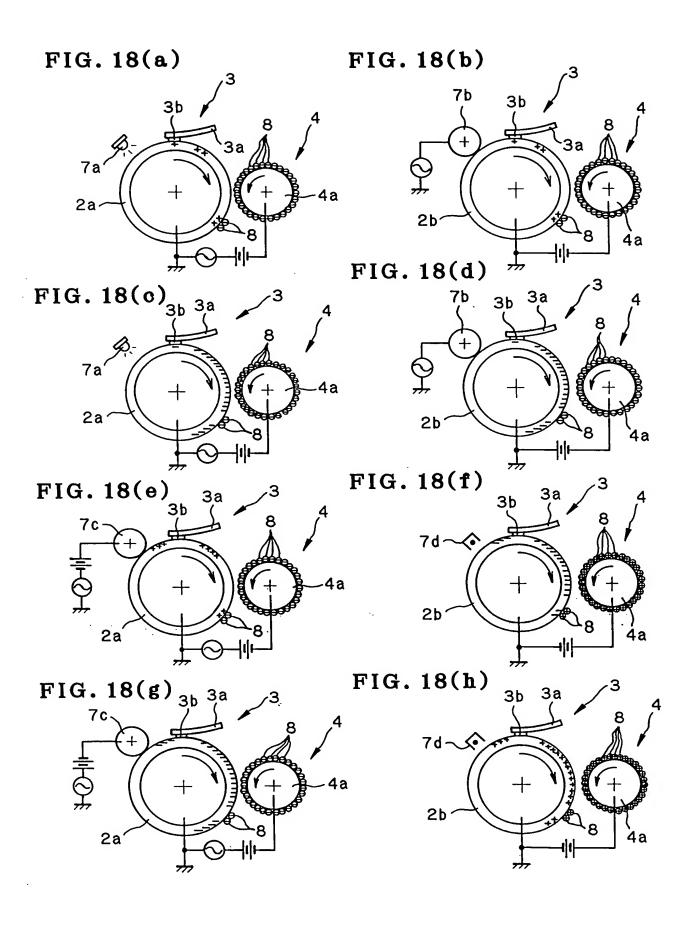


FIG. 19(A)

Applied Voltage V, Writing Time Δt

Electrode

Charge-transferring Direction

Charge Injection Layer

Thickness: d₁

Volume Resistivity: ρ

Dielectric Layer

Thickness: d₂

Dielectric Constant: ε

Conductive Substrate

FIG. 19(B)

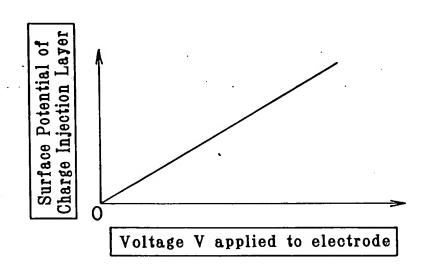


FIG. 20(A)

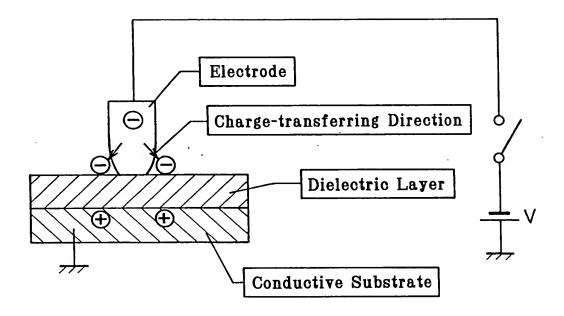
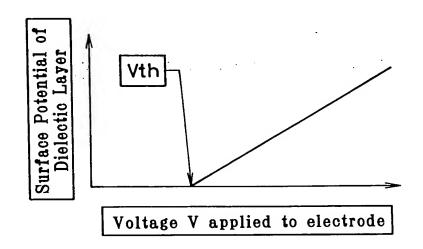


FIG. 20(B)



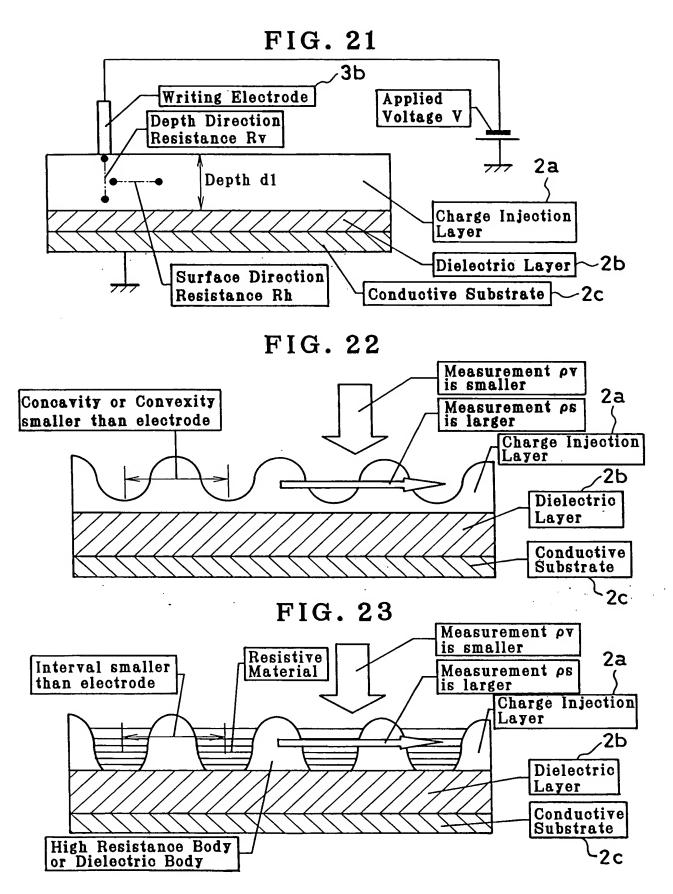


FIG. 24(A)

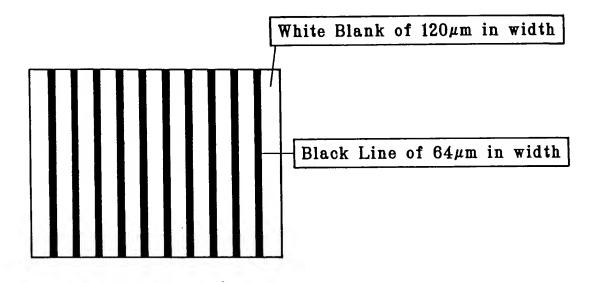


FIG. 24(B)

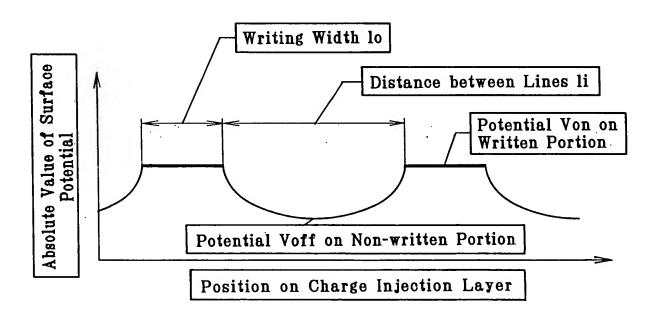


FIG. 25(A)

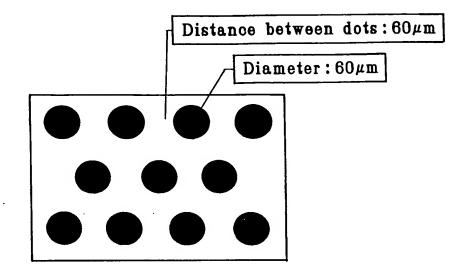


FIG. 25(B)

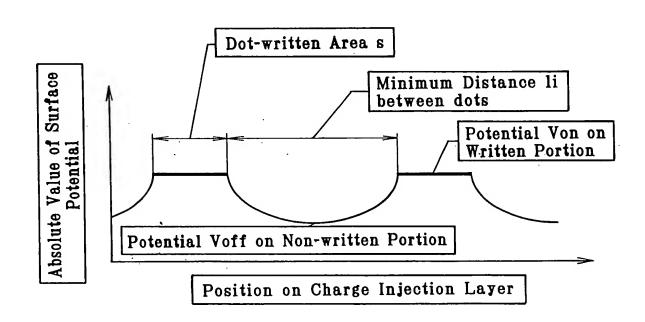


FIG. 26(A)

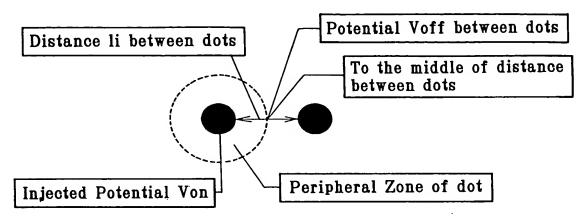
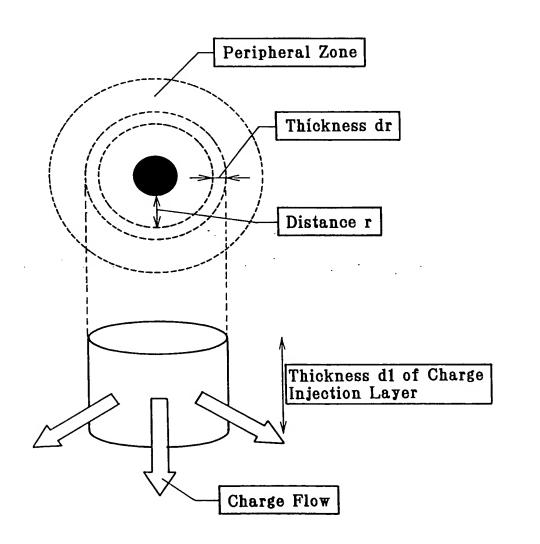


FIG. 26(B)



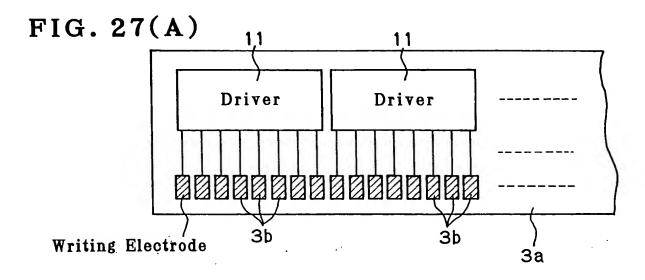


FIG. 27(B)

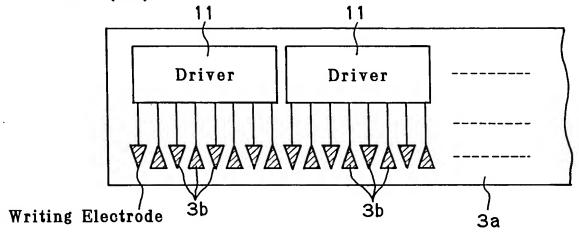


FIG. 27(C)

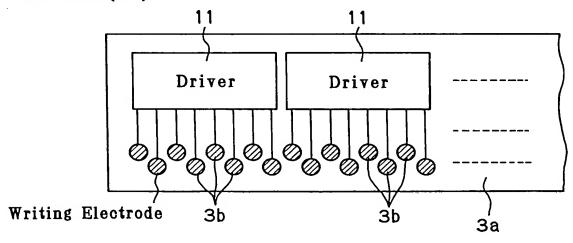


FIG. 28(A)

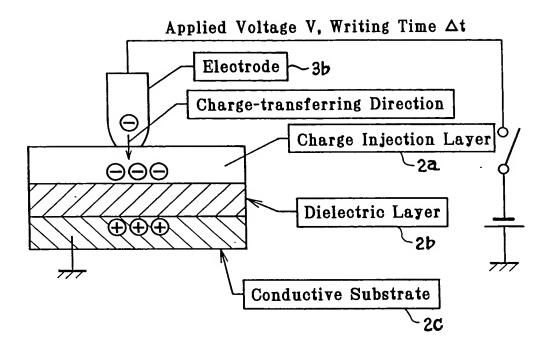


FIG. 28(B)

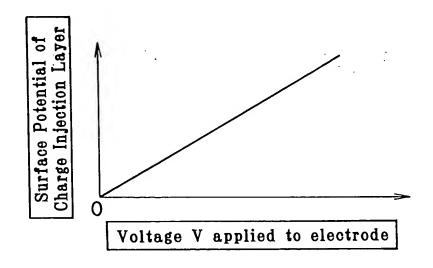


FIG. 29

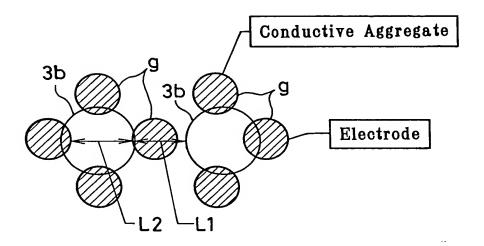


FIG. 30(B) + FIG. 30(A) **6a** +

FIG. 31

